

FIG. 1A is a schematic diagram of a transistor circuit. The circuit includes a voltage source  $V_{source}$  (18) connected to a load resistor  $R_L$  (17) and a transistor (20). The transistor has a gate terminal (22) connected to a gate voltage  $V_{gate}$  (14) and a drain terminal (24) connected to a drain current  $I_D$  (12). The source terminal (26) is connected to a source current  $I_S$  (16). The transistor is represented by a rectangle with a circle inside, and the gate terminal is indicated by a line with a circle at the end.

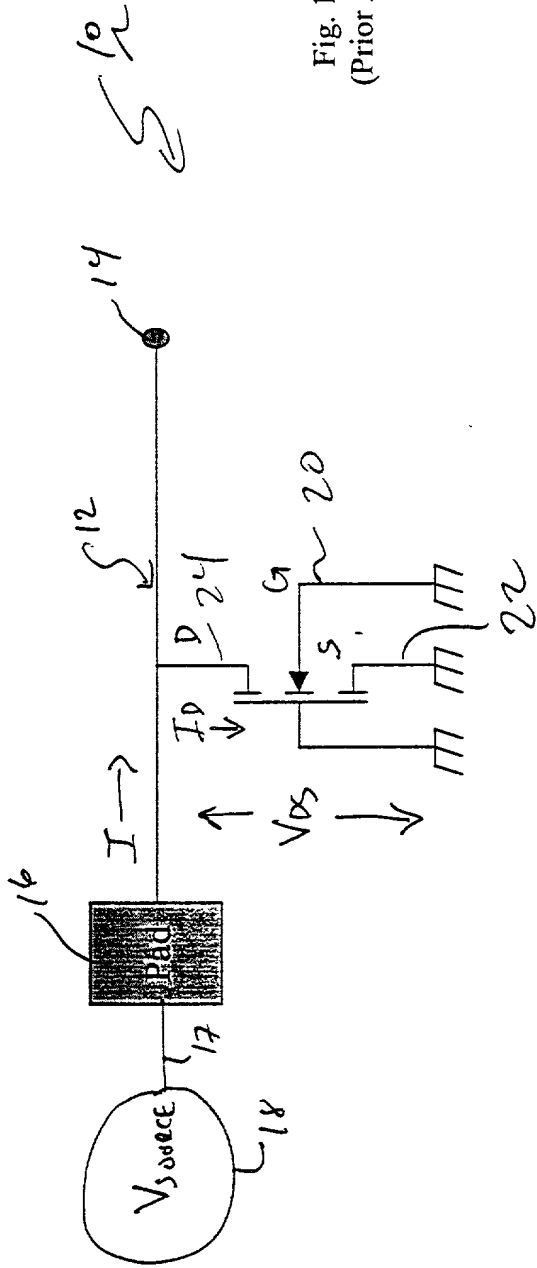


Fig. 1A  
(Prior Art)

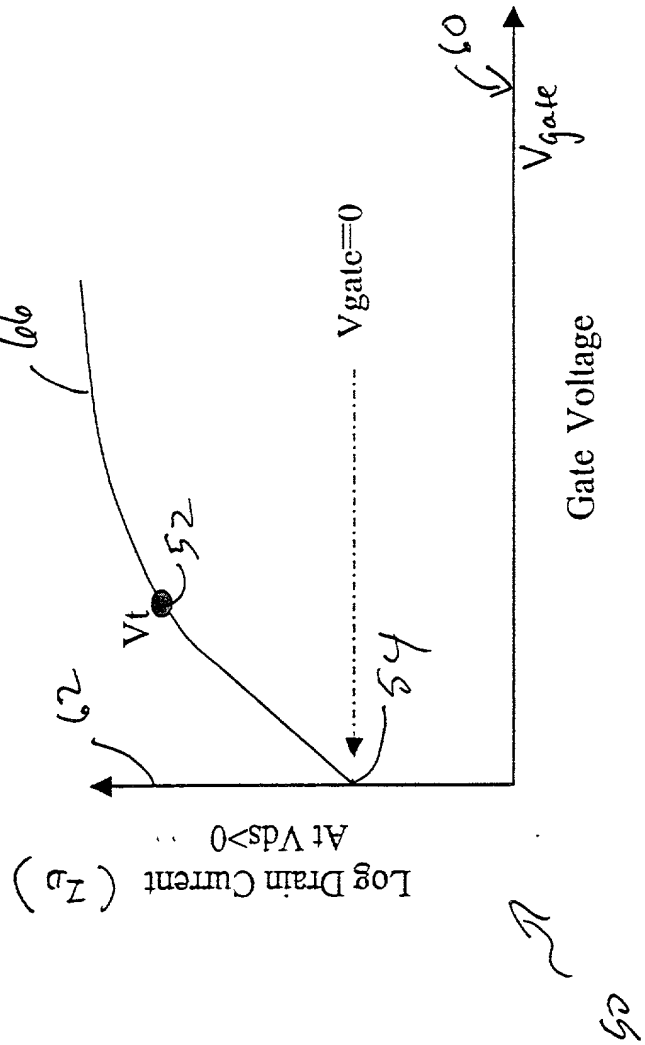


Fig. 1B  
(Prior Art)

FIG. 1C

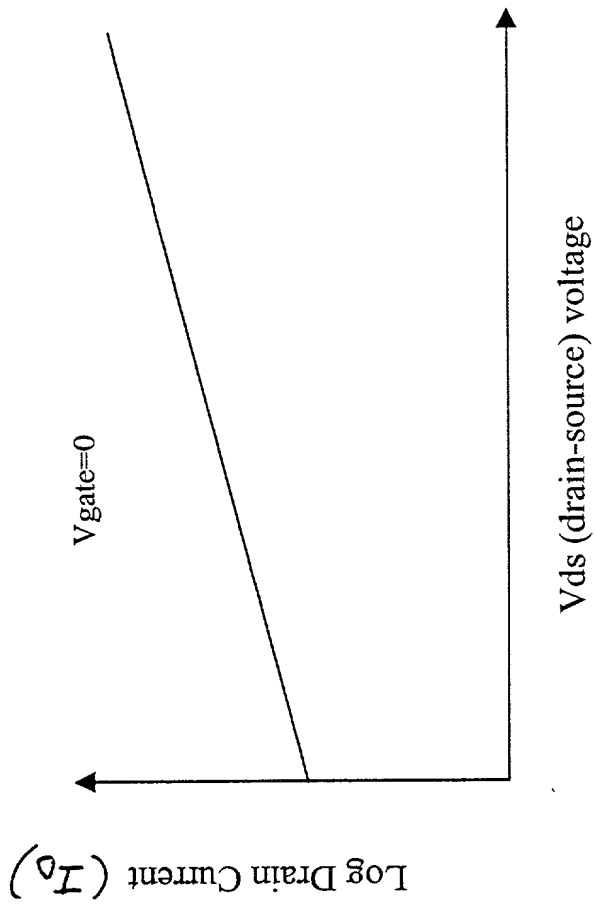


Fig. 1C  
(Prior Art)

Fig. 2A

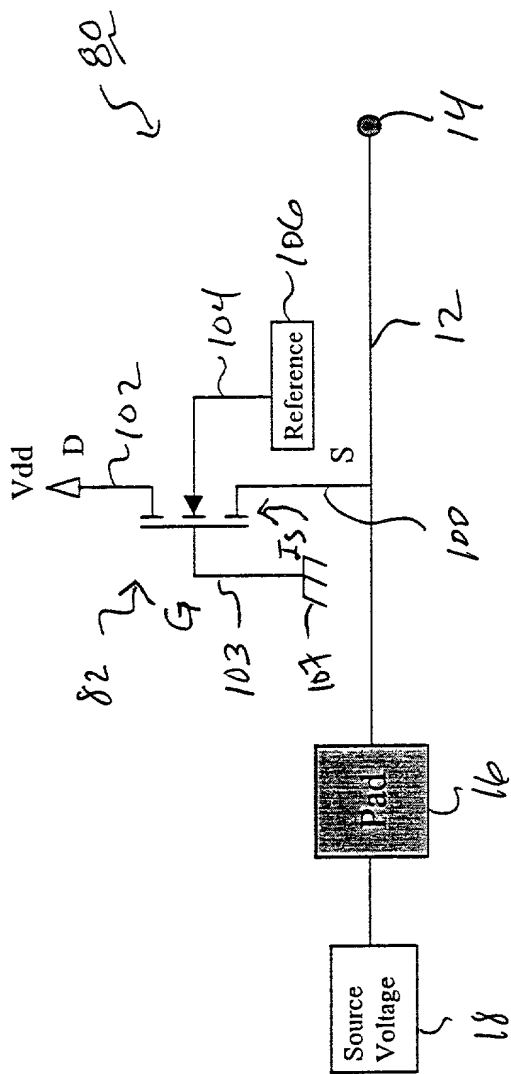
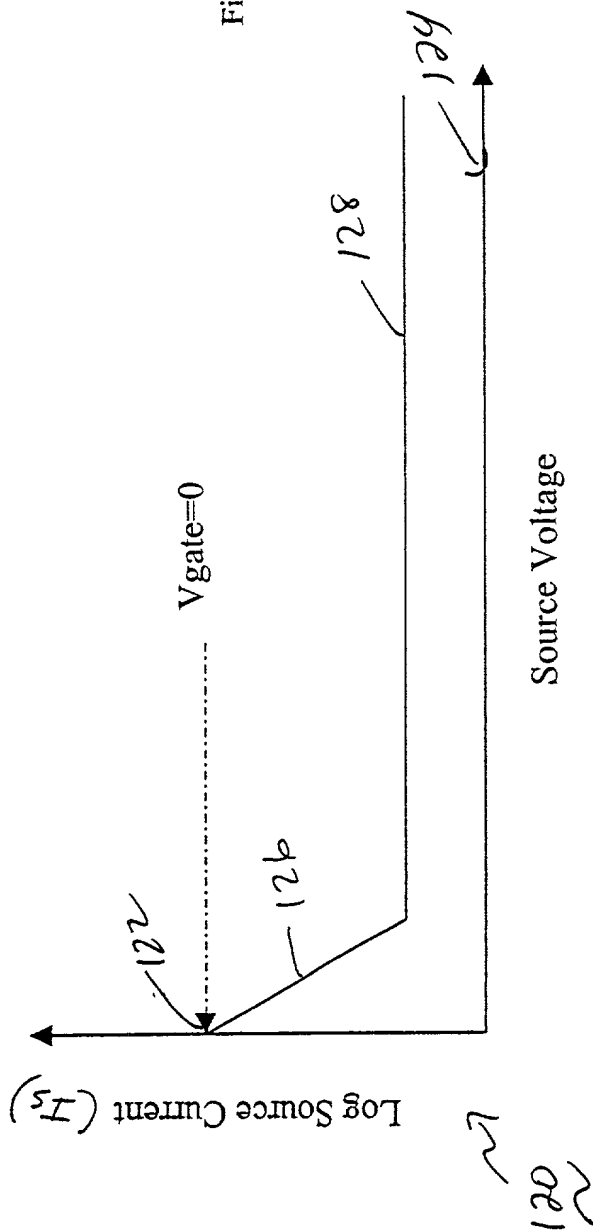


Fig. 2B



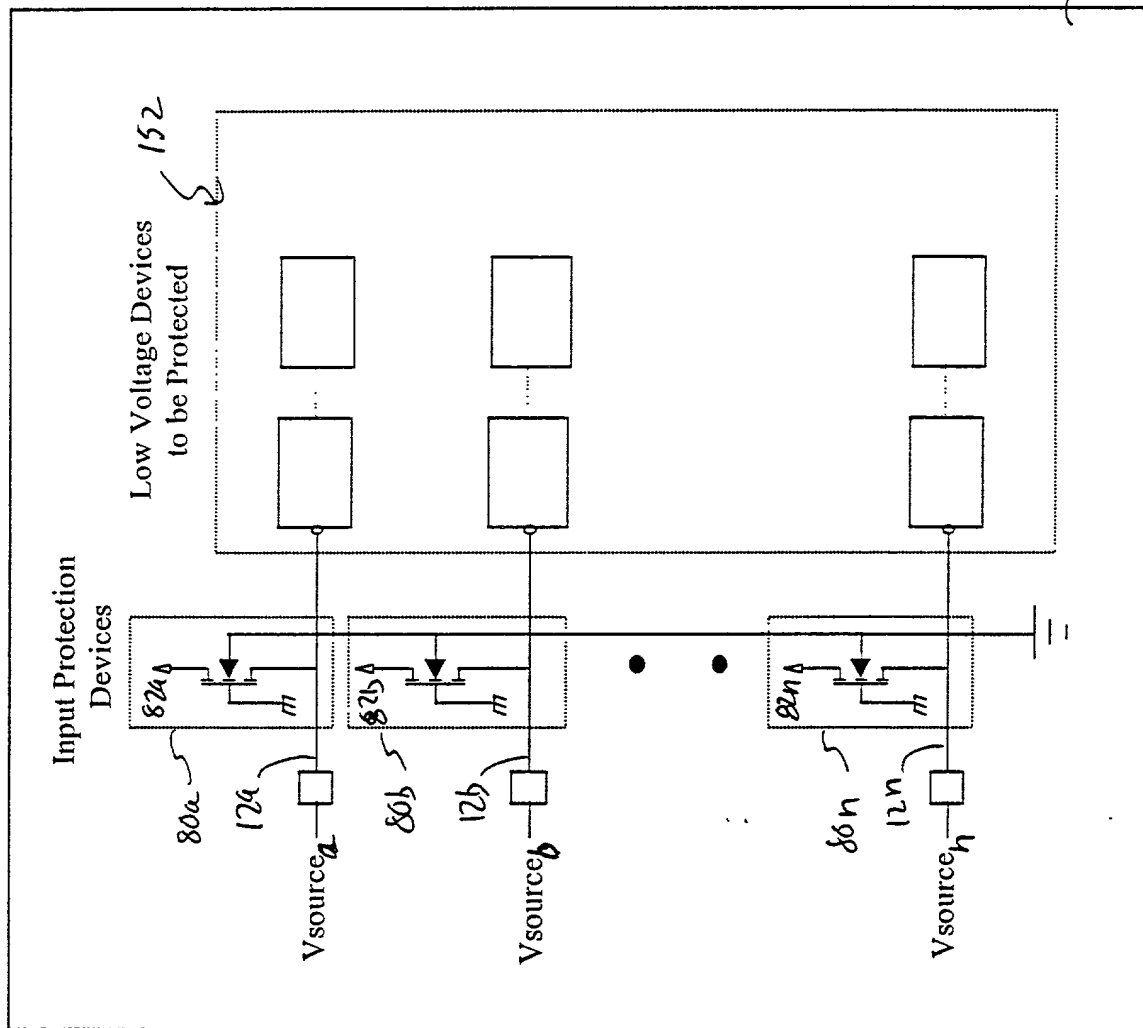


Fig. 3